ASSISTANT COMMISSIONER FOR PATENTS

₩ashington, D.C. 20231

Dear Sir:

Mansmitted herewith for filing is the patent application of:

Inventor(s): Tomoyuki FURUHATA er:

SEMICONDUCTOR DEVICES HAVING A NON-VOLATILE MEMORY TRANSISTOR AND METHODS FOR MANUFACTURING THE SAME

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X	26 Pages of text and 16 Sheet(s) of drawings (formal X informal) + 0 extra copies
<u>X</u>	A certified copy of 2000-382396, filed 15 December 2000, in Japan, from which priority is claimed in the
	subject case pursuant to Rule 55(b) and 35 USC 119 Will follow.
	Declaration and Power of Attorney. X Will Follow.
	Information Disclosure Statement including PTO Form 1449 and copies of references.

		CALCULATION OF FEES					
	ITEM	I.	NUMBER OF CLAIMS FILED MINUS BASE*	NUMBER OF CLAIMS OVER BASE x RATE SM/LG ENTITY	\$ AMOUNT	\$ FEE	
	A	TOTAL CLAIMS FEE	27 - 20* =	x \$9.00 or 7 x \$18.00	\$126		
	В	INDEPENDENT CLAIMS FEE**	9 - 3*=	x \$42.00 or 6 x \$84.00	\$504		
t think and	С						
L	D						
	E	MULTIPLE-DEPENDENT CLAIMS FEE – SMALL ENTITY FEE = \$140.00 /LARGE ENTITY FEE = \$280			\$0		
is	F	ASSIGNMENT RECORDING FEE \$ 40.00			\$0		
# # # F	G	G TOTAL FEE (ADD LINES C, D, E AND F)					
	**LIST INDEPENDENT CLAIMS 1, 6, 10, 11, 15, 23, 24, 25, 27						

- A check in the amount of \$1370.00 to cover the filing fee is enclosed. A copy of this letter is enclosed. <u>X</u>
- <u>X</u>_ The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. 50-0585. A duplicate copy of this sheet is enclosed.
 - Any additional filing fees required under 37 CFR 1.16.
 - Any patent application processing fees under 37 CFR 1.17. \mathbf{X}

Respectfully submitted.

December <u>/</u>4, 2001 Date:

Alan S. Raynes, Reg. No. 39,809

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Customer No. 24033



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re t	the Application of)	
Tomo	yuki FURUHATA)	
Serial	No.: To Be Assigned)) CERTIFICATE OF DEPOS	SIT
Filing	Date: December 14, 2001)	
For:	SEMICONDUCTOR DEVICES HAVING)	
	A NON-VOLATILE MEMORY)	
	TRANSISTOR AND METHODS FOR)	
	MANUFACTURING THE SAME)	

Assistant Commissioner for Patents Washington, DC 20231

Date of Deposit: December 14, 2001

I hereby certify that a utility patent application and patent application fee transmittal; patent application including <u>26</u> pages of text (including an abstract and <u>27</u> claims), <u>16</u> sheets of drawings; an <u>\$1370</u> check for the filing fee; and a <u>certified copy of application number 2000-382395, filed 15 December 2000, in Japan; are being deposited with the United States Patent & Trademark Office on the date indicated above.</u>

Date of Deposit

Date of Deposit

Alan S. Raynes

Name of person delivering papers

Signature